

FIG. 1

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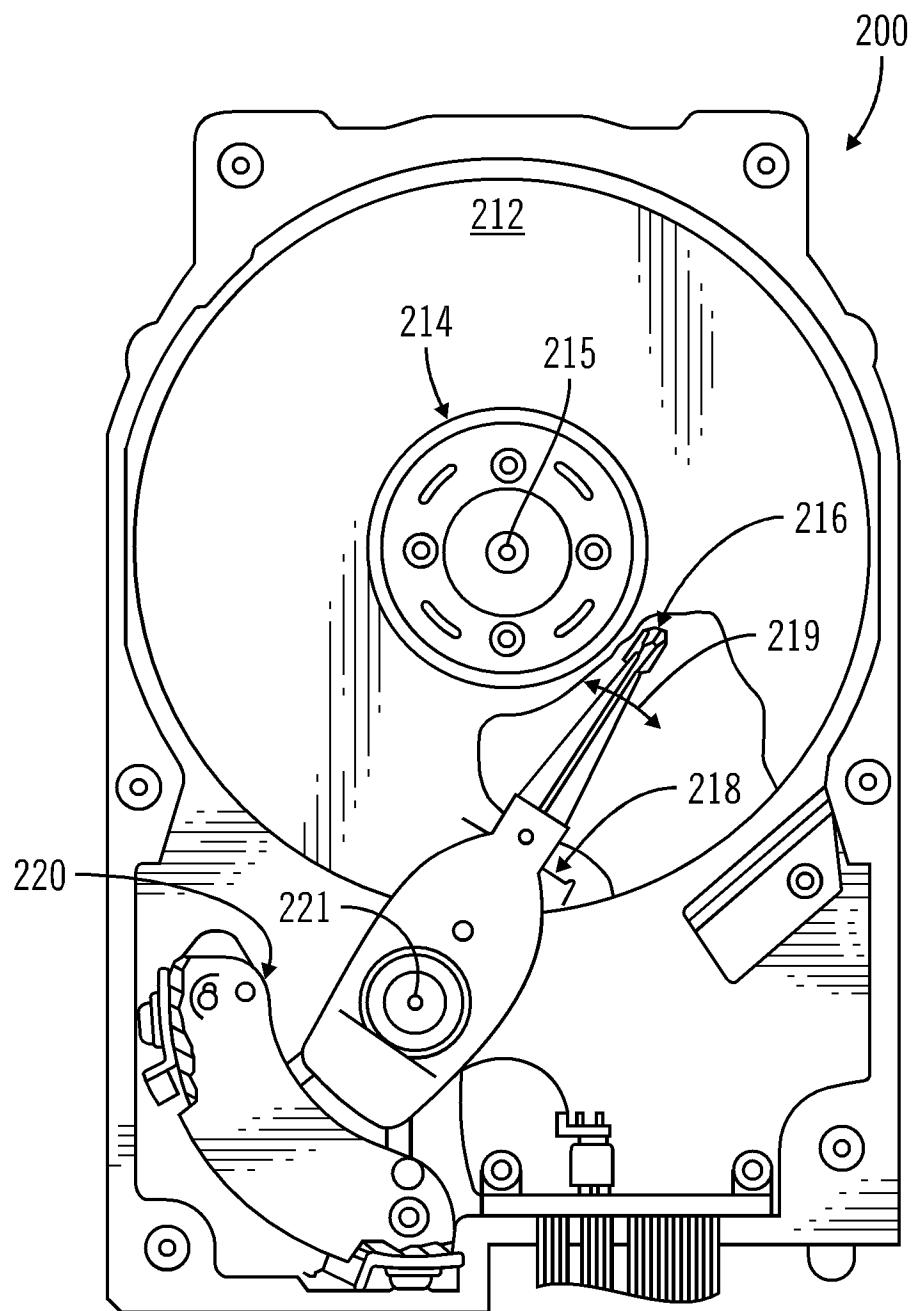
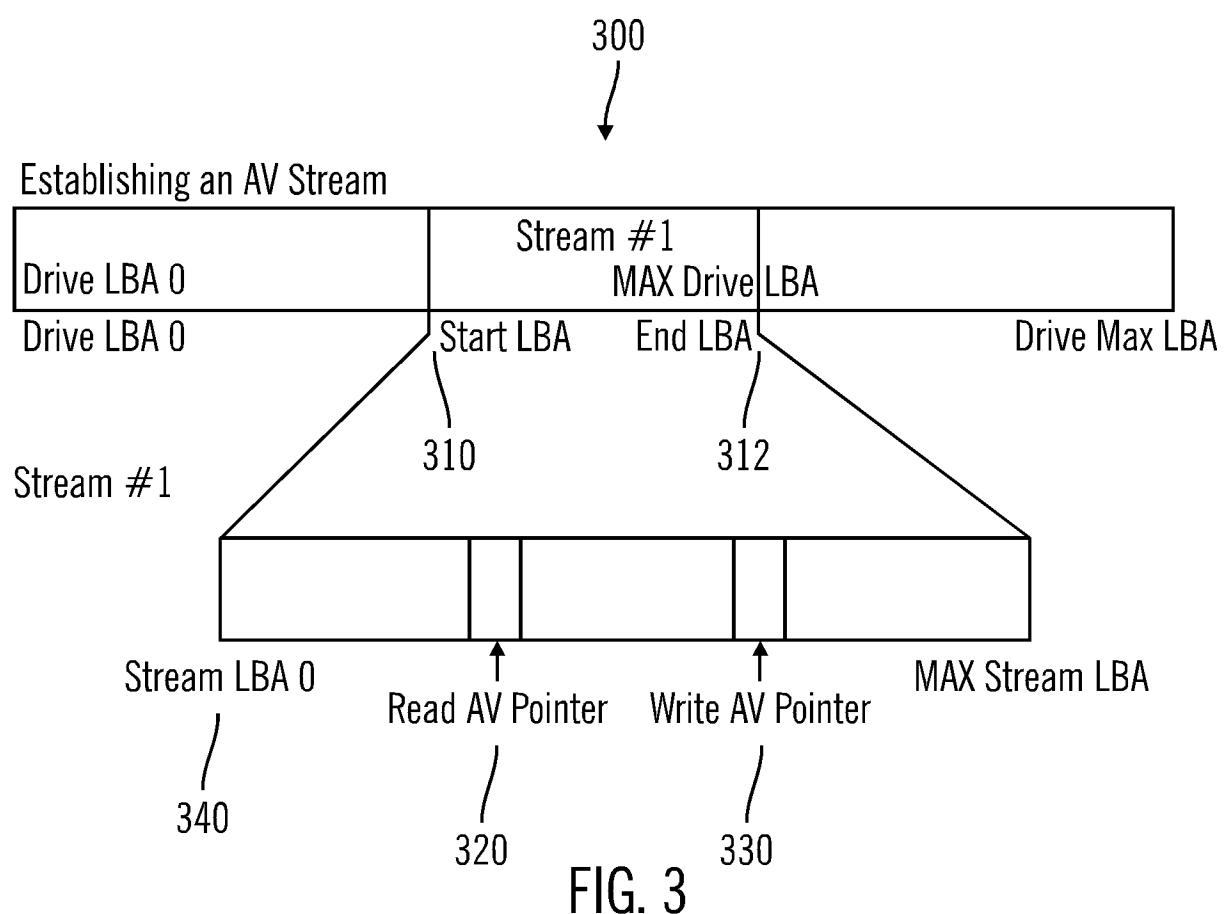


FIG. 2

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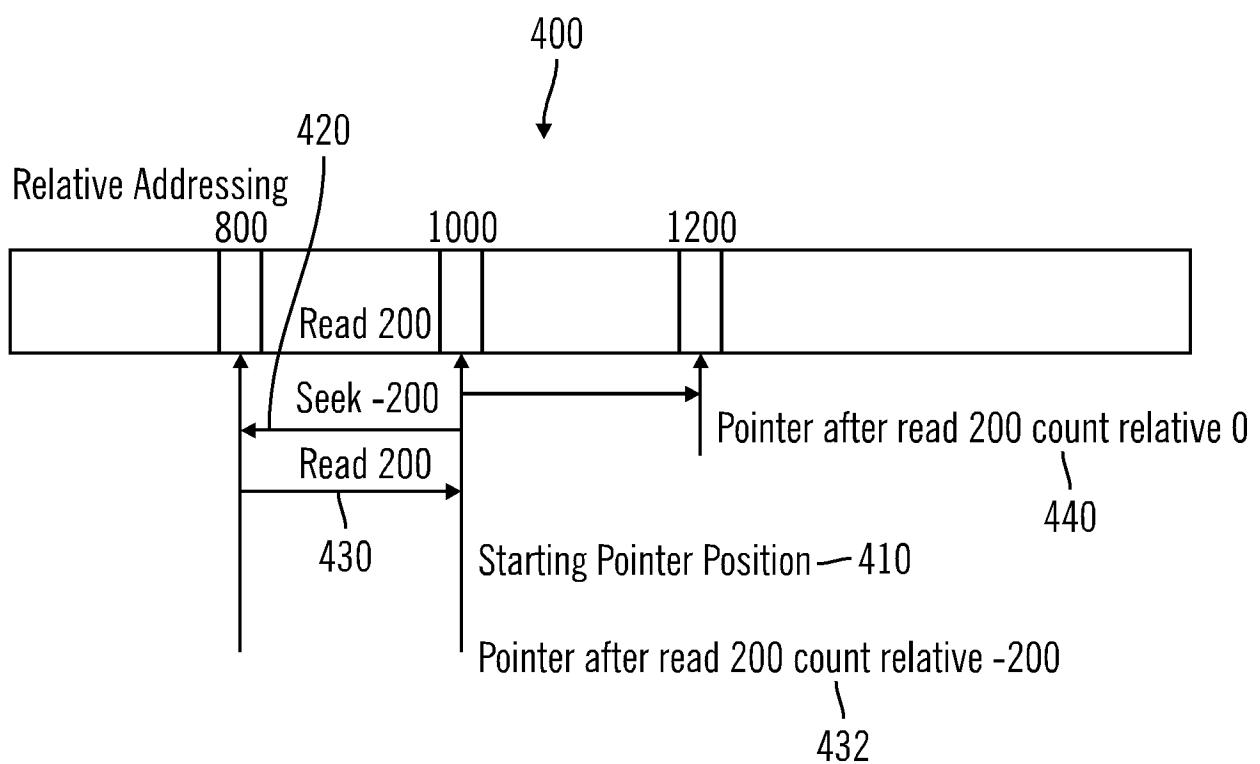


FIG. 4

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Feature Register value	Feature Description
0Bh	Enable AV Stream mode
0Ch	Enable Read Continuous operation
0Dh	Enable Time-base ERP Limitation for Read
0Eh	Enable Time-base ERP Limitation for Write
0Fh	Enable AV Write Cache
8Bh	Disable AV Stream mode
8Ch	Disable Read Continuous operation
8Dh	Disable Time-base ERP Limitation for Read
8Eh	Disable Time-base ERP Limitation for Write
8Fh	Disable AV Write Cache

FIG. 5

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Register	7	6	5	4	3	2	1	0
Features					0Bh			
Sector Count					na			
Sector Number					na			
Cylinder Low					na			
Cylinder High					na			
Device/Head	obs	na	obs	DEV			na	
Command					EFh			

FIG. 6

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The diagram illustrates a 16-bit register structure. The register is divided into four fields: 700 (bits 7-4), 710 (bits 3-0), 720 (bits 7-4), and 730 (bits 3-0). The 700 field contains the value 0Bh. The 710 field contains the value na. The 720 field contains the value ER. The 730 field contains the value na. The register is mapped to the following table:

Register	7	6	5	4	3	2	1	0
Features					0Bh			
Sector Count					na		ER	
Sector Number					na			
Cylinder Low					na			
Cylinder High					na			
Device/Head	obs	na	obs	DEV			na	
Command					EFh			

FIG. 7

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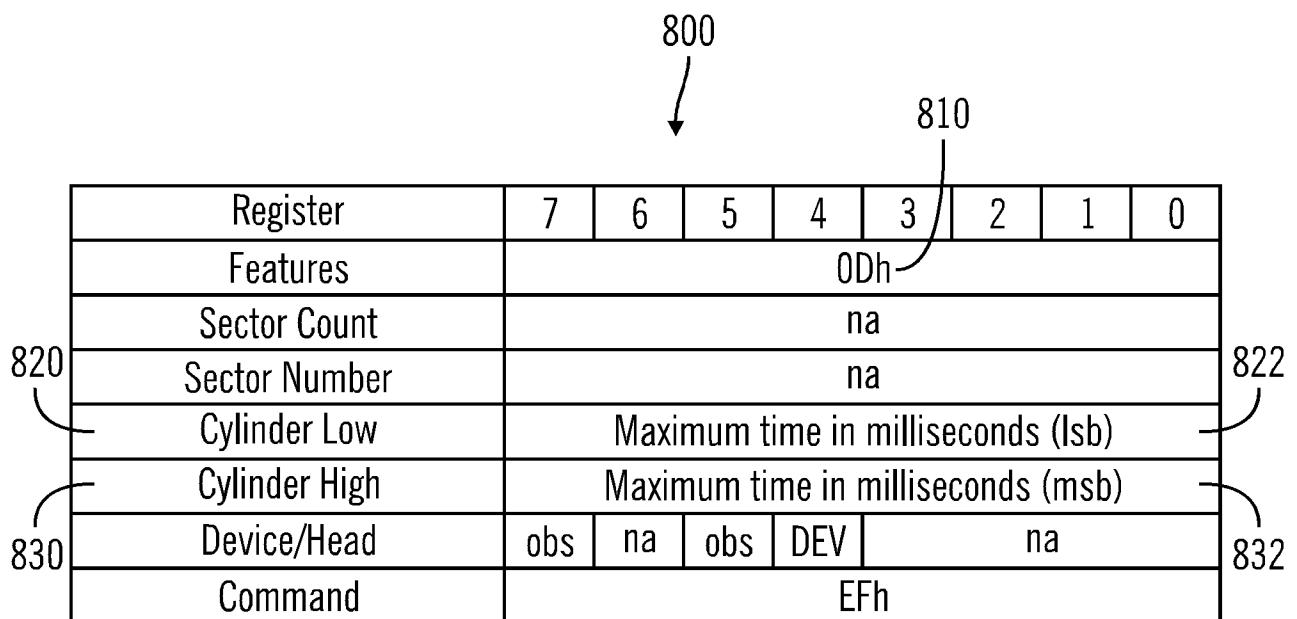


FIG. 8

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Diagram illustrating the structure of a 16-bit register (900 bytes long) and its fields:

Register	7	6	5	4	3	2	1	0	
Features	0Eh							912	
Sector Count	na							916	
Sector Number	na							ER	
Cylinder Low	Maximum time in milliseconds (lsb)							922	
Cylinder High	Maximum time in milliseconds (msb)							922	
Device/Head	obs	na	obs	DEV	na				932
Command	EFh								932

FIG. 9

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Register	7	6	5	4	3	2	1	0
Features								na
Sector Count								na
Sector Number								na
Cylinder Low								na
Cylinder High								na
Device/Head	obs	na	obs	DEV				na
Command							FAh	

FIG. 10 1020

Register	7	6	5	4	3	2	1	0
Error	na	na	na	na	na	ABRT	na	na
Sector Count								na
Sector Number								na
Cylinder Low								na
Cylinder High								na
Device/Head	obs	na	obs	DEV				na
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 11

1000

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	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	Stream Number															
1	Link Stream Number															
2	reserved															
3	reserved															
4	Stream Starting LBA															
5	1230															
6	1240															
7																
8	Stream Ending LBA															
9	1242															
10																
11																
...	...															
255	reserved															

FIG. 12

Register	7	6	5	4	3	2	1	0
Features	na							
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Command	FAh							

FIG. 13

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Register	7	6	5	4	3	2	1	0
Error	na	na	na	na	na	ABRT	na	na
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 14

1500

Register	7	6	5	4	3	2	1	0
Features	AV Stream Number							
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Command	FCh							

FIG. 15

1600

Register	7	6	5	4	3	2	1	0
Error	na	na	na	na	na	ABRT	na	na
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 16

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1700 →      1710

Register	7	6	5	4	3	2	1	0
Error	na	na	na	na	na	ABRT	na	na
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 17

1820

1824 →

Register	7	6	5	4	3	2	1	0
Features	AV Stream Number							
Sector Count	Function Code							
Sector Number	LBA							
Cylinder Low	LBA							
Cylinder High	LBA							
Device/Head	obs	LB A	obs	DEV	LBA			
Command	C3h							

FIG. 18

1822

1920 →      1900 →

Value in Sector Count	Action
0	Set the Read AV Pointer
1	Set the Write AV Pointer
2	Return the Read AV Pointer
3	Return the Write AV Pointer
4-255	Reserved

1910  
1908

FIG. 19

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2000

Register	7	6	5	4	3	2	1	0
Error	Stream Number							
Sector Count	Function Code							
Sector Number	LBA							
Cylinder Low	LBA							
Cylinder High	LBA							
Device/Head	obs	LBA	obs	DEV	LBA			
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 20

2100

Register	7	6	5	4	3	2	1	0
Error	na	na	na	na	na	ABRT	na	na
Sector Count	na							
Sector Number	na							
Cylinder Low	na							
Cylinder High	na							
Device/Head	obs	na	obs	DEV	na			
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 21

2200

Register	7	6	5	4	3	2	1	0
Features	AV Stream Number							
Sector Count	Sector count							
Sector Number	Relative Address							
Cylinder Low	Relative Address							
Cylinder High	Relative Address							
Device/Head	obs	LB A	obs	DEV	Relative Address			
Command	CEh							

FIG. 22

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2300

Register	7	6	5	4	3	2	1	0
Error					na			
Sector Count					na			
Sector Number					LBA			
Cylinder Low					LBA			
Cylinder High					LBA			
Device/Head	obs	na	obs	DEV			LBA	
Status	BSY	DRDY	DF	na	DRQ	na	PP	ERR

FIG. 23

2310

2400

Register	7	6	5	4	3	2	1	0
Error	ICRC	WP	MC	IDNF	MCR	ABRT	NM	na
Sector Count					na			
Sector Number					na			
Cylinder Low					na			
Cylinder High					na			
Device/Head	obs	na	obs	DEV			na	
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 24

2500

2520

Register	7	6	5	4	3	2	1	0
Features					AV Stream Number			
Sector Count					Sector count			
Sector Number					Relative Address			
Cylinder Low					Relative Address			
Cylinder High					Relative Address			
Device/Head	obs	LB A	obs	DEV		Relative Address		
Command					CFh			

2540

2510

FIG. 25

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2600

Register	7	6	5	4	3	2	1	0
Error					na			
Sector Count					na			
Sector Number					na			
Cylinder Low					na			
Cylinder High					na			
Device/Head	obs	na	obs	DEV			na	
Status	BSY	DRDY	DF	PP	DRQ	na	na	ERR

2610

FIG. 26

2700

2710

Register	7	6	5	4	3	2	1	0
Error	ICRC	WP	MC	IDNF	MCR	ABRT	NM	DFER
Sector Count					na			
Sector Number					LBA			
Cylinder Low					LBA			
Cylinder High					LBA			
Device/Head	obs	na	obs	DEV			LBA	
Status	BSY	DRDY	DF	na	DRQ	na	na	ERR

FIG. 27

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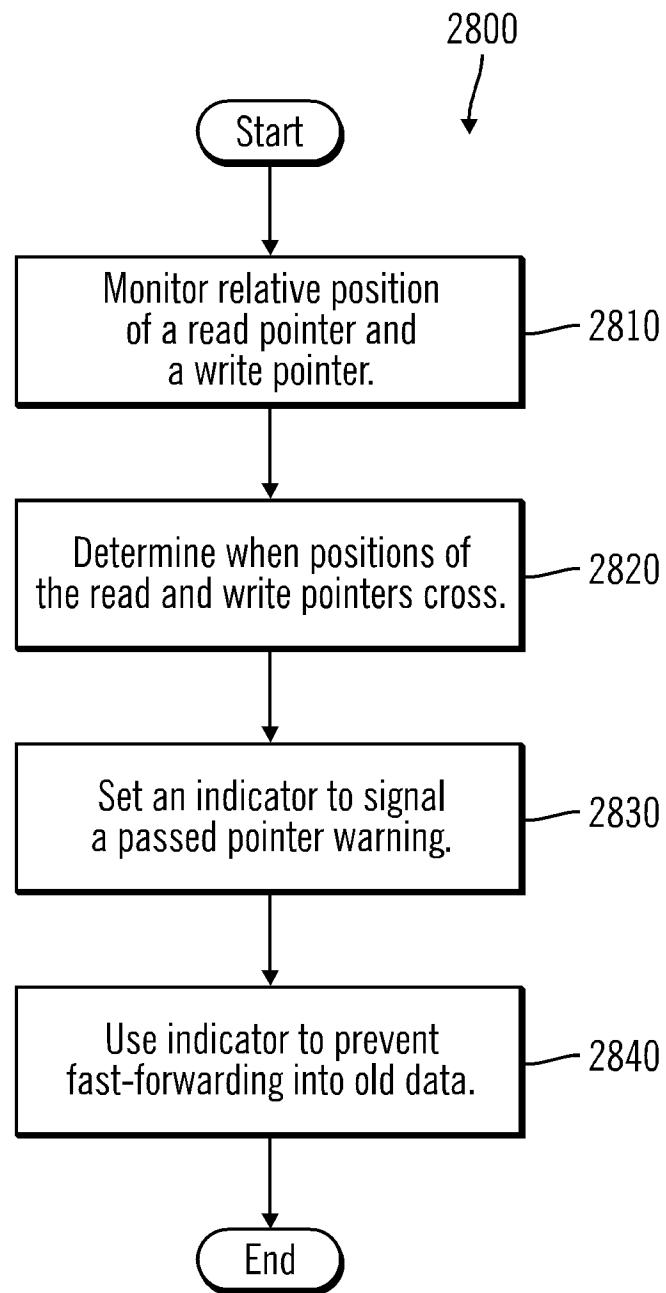


FIG. 28